May 11, 1959

MEMORANDUM TO:

Mr. P. Parbanish

SUBJECT:

"Transfer Program" Codes

Memo to 1401 Files - "Transfer Program" Codes 11 May 1959 -

Attached is a logic diagram which is intended to indicate the manner in which the compare circuit is to operate. I believe that it is essentially correct, with the possible exception of the compare latent with the possible exception of the compare latent properties of the control is required to take care of unequal length fields.

Flease investigate and incorporate the best design into the release design.

F. O. Underwood

FU/mh

