MEMORANDUM TO:

effective immediately:

1401 Personnel

Records and Procedures Superseies previous Nemo. of 4/16/59

In order to facilitate the keeping of proper records and expedite wiring changes, the following procedures are

## Tape Edit System Engineering Model Records

- When a designer authorizes a wire list to be processed by the computer, Further changes to the most be processed until the wired changis is received. These changes are to be accumulated and recorded on the "GPFIGIAL" prints provided for this purpose in combination with "ADD" and "REMOVE" wire list force.
- The above procedure results in the logic diagram veilums and original wire lists not being altered during the period mentioned.
- 3. Circuit changes on chassis in the model shop must be accompanied by "ADD" and "REMOVE" wire lists and the changes to the "OFFICIAL" logic diagram prints. In order to reduce the time required, it has been decided that only not no., wire no., wire level, from and to information are uncersary on un

## Cost Reduction Engineering Records

Each gate will be analyzed with cost reduction in mind and mew logic diagrams originated members were supported by the logic diagram originated with the logic diagram ammbering system will be used, change level numbers of 2000 and up will be malegad for use made existing logic diagram numbering system will be used, change level numbers over 2000 and up will be malegad for use made a contract the logic machine and the logic machine will be processed through connected sheet runs with wire lists deferred until chamais are released or order to the logic members will be the logic members of the logic members will be release version of the TSS.

The following records and wiring procedures apply:

- If wired chassis are ordered for the Optional Features Model before release of the Cost Reduction logics, then paragraphs 1 and 2, as outlined under the TES Engineering Model, shall be followed.
- 2. However, circuit changes in the model shop on the above chassis must not only be accompanied by "ADD" and "REMOVE" wire lists and the changes to the "CPPICIAL" logic diagram prints; but all information, including wire lengths and via points, is necessary on the "ADD " lists.

R. A. Rowley

R.R./mh