

I CYCLE
GATED WM

OVERLAP BUSY

RD+PCH OP

I RING 2+I RING 5

BILL FEED OP LT

COL BIN OP LT

UPPER OPTK

T+1/2 OVL MODE

PRINT OF (NPF)

A REG 8H

A REG 8T

A REG AB

A REG 5B

RD OP

PCH OP

NOT RD COMP

NOT PCH COMP

B REG 8

L REG 8Y

B REG 8I

I CYCLE

I RING 1

T+1/2 UNIT SEL

NOT T+1/2 OVL MODE

OVERLAP PROCESS UNIT

TIME 000-075

START RESET

TIME 079-087

OVL ALL SCANS COMP

(028L)

(NPF) PR. ERROR

1/2 CHK STOP ON

(02) BRANCH

(028L) OVL NOT BUSY

(02) I CYCLE

(02) GATED WM

(02) RD+PCH OP

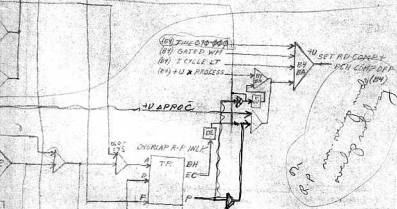
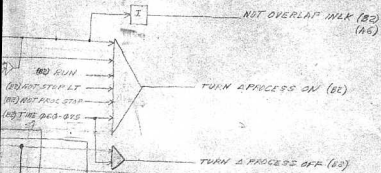
(02) NOT PRINT OF

(028L) NOT R-P SW. MODE

(02,86) PRINT SCAN END

(028L) +U NOT OVL ALL SCANS COMP

PCA



ΔP *man*

E. M. *...*