MEMO TO: 1401 File

SUBJECT: Minutes of Meeting Concerning 1401 Sync Points and Stop Clock Facility

ATTENDED BY: Messrs. K. Bell
J. Conzola
D. Crofford
H. Gebow
J. Girvin
J. Pokosk
O. Smith

It was agreed by all attendees that the lower (Customer Engineering) console panel on future 1401 systems should contain the following sync points and Customer Engineering alds.

1. +U Time 000
2. +U Address Sop
3. +U Read Stan
4. +U K Gate
5. +U Start Latch
7. +U Start Latch
7. +U Not First Scan

8. +U Punch Scan

9. +U Y Gate

10. +U Delta Overlap Cycle

+U Delta Process Cycle
 A toggle switch which will stop the 1401 clock on any process error.

process error.

13. A "Stop Clock" hub which when conditioned on, will force a false op register error and stop the clock, if the stop clock switch is on.

 A "Two way CTDL AND" circuit of such level that its output can be jumpered to the stop clock hub. (Three hubs required.)

Messrs. Crofford and Girvin agreed that the circuits described met their requirements for stopping the clock as a Customer Engineering feature. Mr. Smith agreed to supply personnel to process the sync point changes and changes in the mechanical assembly of the console.

It was decided that Mr. Pokoski would implement the stop clock circuitry.

John Pokoski Dept. 280

JP/ka

cc: Mr. F. Baldwin Mr. R. Chandler

Mr. J. Ingram Mr. W. Wheeler

Mr. W. Wheeler